EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	(IP$core) with (add$3 near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 17:46
S2	1	(IP$core electronic) same (obfuscat$ near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 17:59
S3	0	(IP$core (electronic near2 design)) and (obfuscat$ near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 18:00
S4	0	(IP$core) and (obfuscat$ near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 18:00
S5	0	IP$core and (obfuscat$ near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 18:01
S6	14	(IP$core (function$2 near2 block) hardware IC) and (obfuscat$ near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/08 18:33
S7	1	("7107567").PN.	USPAT; USOCR	OR	OFF	2007/05/08 18:33
S8	4	("20030188275" | "20040088674" | "20050071792" | "6539525").PN. OR ("7107567").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/05/08 18:35
S9	15	(IP$core (electronic near2 design)) and ((obfuscat$ entangl$3 scrambl$3) near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 19:06
S10	13	(IP$core HDL VHDL verilog AHDL) and ((obfuscat$ entangl$3 scrambl$3) near2 circuit$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 19:14

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S11	49	(IP$core HDL VHDL verilog AHDL) and ((insert$3 add$3) with (entangl$3 scrambl$3 obfuscat$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 19:15
S12	2	(IP$core HDL VHDL verilog AHDL) and ((insert$3 add$3) near (entangl$3 scrambl$3 obfuscat$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 19:40
S13	0	("70802257").PN.	USPAT; USOCR	OR	OFF	2007/05/08 19:30
S14	1	("7080257").PN.	USPAT; USOCR	OR	OFF	2007/05/09 09:04
S15	37	("20010037450" | "20050210275" | "3745316" | "4172213" | "5054787" | "5123045" | "5126728" | "5159630" | "5199069" | "5365589" | "5420942" | "5548648" | "5742686" | "5745569" | "5748741" | "5768372" | "5809306" | "5812671" | "5852664" | "5915017" | "5933498" | "5933501" | "5960080" | "6041316" | "6085029" | "6240183" | "6256777" | "6263313" | "6483600" | "6507868" | "6636530" | "6668325" | "6779114" | "6782478" | "6801999" | "6901516" | "7000119").PN. OR ("7080257").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/05/08 19:33
S16	1	"20020138244"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/08 19:40
S17	1	("7165229").PN.	USPAT; USOCR	OR	OFF	2007/05/09 09:09
S18	3	("20030093655" | "5841663" | "6976239").PN. OR ("7165229").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/05/09 09:05
S19	0	simulat$4 with (IP$core) with (protect$4 secur$3 obfuscat$4 (revers$3 near2 engineer$3) entangl$3 scrambl$3) with (hardware near3 design)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:12
S20	14799	simulat$4 same (hardware HDL VHDL Verilog IP$core)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:13

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S21	8641	simulat$4 with (hardware HDL VHDL Verilog IP$core)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:13
S22	4602044	(protect$4 secur$3 obfuscat$4 (revers$3 near2 engineer$3) entangl$3 scrambl$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:13
S23	2688	S21 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:14
S24	223	S23 and (EDA (electronic adj design))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:14
S25	5	S24 and obfuscat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:24
S26	95	S24 and (flip$flop$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 09:25
S27	86	S26 and (optimiz$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 10:40
S28	1	("6401230").PN.	USPAT; USOCR	OR	OFF	2007/05/09 11:45
S29	1	(simulat$4 model$3) with (IP$core)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:47
S30	3133	(simulat$4 model$3) with (IP$core verilog HDL vhdl ahdl)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:48

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S31	187097	(protect$3 encrypt$4 decrpyt$4 entangl$3 (revers$3 near2 engineer$3)) with (hardwar$2 circuit$3 IC (integrated adj circuit$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:51
S32	129	S30 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:58
S33	62	S32 and (flip$flop)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:52
S34	62	S32 and (flip$flop and logic)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:52
S35	59	S32 and (flip$flop and compil$5 and synthesi$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:53
S36	0	S32 and (flip$flop and compil$5 and synthesi$4 and entangl$3 and scrambl$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:53
S37	46	S32 and (flip$flop and compil$5 and synthesi$4 and scrambl$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 11:54
S38	44	S32 and @pd<"20032208"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/05/09 12:00
S39	1	"20030145177"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:23
S40	0	((hardware electronic) near design) same (simulat$4 model$3) same (synthesiz$3 compil$5) same (obfuscat$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:25

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S41	2	((hardware electronic) near design) same (simulat$4 model$3) same (synthesiz$3 compil$5) and (obfuscat$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:26
S42	54	((hardware electronic) near design) same (simulat$4 model$3) same (synthesiz$3 compil$5) and (protect$3 secur$3 entangl$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:27
S43	5	((hardware electronic) near design) same (simulat$4 model$3) same (synthesiz$3 compil$5) same (protect$3 secur$3 entangl$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:40
S44	0	simulat$4 with (IP$core) with (protection secur$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:41
S45	0	simulat$4 with (IP$core) same (protection secur$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:41
S46	0	simulat$4 same (IP$core) same (protection secur$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:42
S47	0	simulat$4 same (IP$core) same (protection secur$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:42
S48	10	simulat$4 same (IP$core hdl vhdl) same (protection secur$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2007/05/09 16:42
S49	1	("6049787").PN.	USPAT; USOCR	OR	OFF	2007/05/10 09:27